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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10050171	FILING DATE 01/18/2002	CLASS 259 428	SUBCLASS 503	GAU 2811 2812	EXAMINER <i>Watanabe</i>
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**APPLICANTS: Watanabe Kenichi;

Watanabe

**CONTINUING DATA VERIFIED:

None

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-271416 09/07/2001 *MEW*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 020029
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials <i>MEW</i>			
TITLE : Semiconductor device capable of suppressing current concentration in pad and its manufacture method			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-438L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
			Print Fig.
<input type="checkbox"/> TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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